



THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	02/19/03	Name of Person Making the Deposit:	Julie Williams
		Signature of the Person Making the Deposit:	<i>Julie Williams</i>

In re Application of: Steve Roe and Craig Nemecek

Serial No.: 10/001,477

Examiner:

Filed: 11/01/01

Art Unit:

For: BREAKPOINT CONTROL IN AN IN-CIRCUIT EMULATION SYSTEM

Assistant Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2100

AMENDMENT TRANSMITTAL

1. Transmitted herewith is an amendment for this application

- ☒ Transmitted herewith is a response to an office action for the above identified patent application.
(8 sheets)
Transmitted herewith are _____ sheets of substitute formal drawings.
☒ Other: Supporting Documents

2. Applicant is other than a small entity

Extension of Term

3. The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.

- (a) [] Applicant petitions for an extension of time under 37 C.F.R. 1.136
(fees: 37 C.F.R. 1.17(a)-(d) for the total number of months checked below:)

Extension	Fee
[] one month	\$110.00
[] two months	\$410.00
[] three months	\$930.00
[] four months	\$1,450.00

Fee \$ _____

If an additional extension of time is required, please consider this a petition therefor.

- (b) [X] Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

Fee Calculation

4. The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small entity)					
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total
Total Claims	20	- 20 =	0	x \$18.00	\$0.00
Independent Claims	3	- 3 =	0	x \$84.00	\$0.00
Multiple Dependent Claim Fee (one or more, first added by this amendment)				\$260.00	\$0.00
Total Fees					\$0.00

PAYMENT OF FEES

5. The full fee due in connection with this communication is provided as follows:
- ☒ The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
- ☐ A check in the amount of \$
- ☐ Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:


WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date:

2/19/03

By:


Anthony C. Murabito
Reg. No. 35,295



CD01203M

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :
Steve ROE et al. : GROUP ART UNIT:
SERIAL NO: 10/001,477 :
FILED: NOVEMBER 1, 2001 : EXAMINER:
FOR: BREAKPOINT CONTROL IN AN
IN-CIRCUIT EMULATION
SYSTEM

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph beginning at page 4, line 9 with the following:

A third technique, one that is used in the PENTIUM® and PENTIUM PRO™ series of microprocessors available from Intel Corporation, provides a special “probe mode” of operation of the processor. When the processor is placed in this probe mode, a number of internal signals are routed to a “debug port” for use by the in-circuit emulation system. This debug port is used to permit the in-circuit emulation system to communicate with the processors at all times and, when placed in probe mode, to read otherwise inaccessible probe points within the processor. Of course, providing such a probe mode requires significant design resources to design in all such probe and

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